

Notice of References Cited	Application/Control No. 09/990,935		Applicant(s)/Patent Under Reexamination LAM, WILLIAM	
	Examiner Thomas H. Stevens		Art Unit 2123	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-4,916,647	04-1990	Catlin, Gary M.	703/16
	B	US-5,095,454	03-1992	Huang, Chi-Lai	703/19
	C	US-6,295,513	09-2001	Thackston, James D.	703/1
	D	US-5,600,263	02-1997	Trimberger et al.	326/39
	E	US-5,678,028	10-1997	Bershteyn et al.	703/22
	F	US-5,937,183	08-1999	Ashar et al.	703/14
	G	US-6,339,837	01-2002	Li, Zhe	716/5
	H	US-6,856,950	02-2005	Abts et al.	703/13
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
	U	Cheng et al., "Synthesizing Multi-Phase HDL Programs" IEEE 1996 abstract. pg. 1 of 1.			
	V	Krishnaswamy et al., "Parallel Compiled Event Driven VHDL Simulation" 1998 ICS Melbourne Australia ACM pg.297-304			
	W				
	X				

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.